



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kim, et al. Docket No: TI-29012
Serial No: 09/574,653 Conf. No: 8503
Examiner: Hsien Ming Lee Art Unit: 2823
Filed: 05/18/2000
For: A TUNABLE SIDEWALL SPACER PROCESS FOR CMOS INTEGRATED CIRCUITS


AF
2800
#10
Amend
B
(PVE)
1-14-03
arg
2/4/03
V. Short

AMENDMENT PURSUANT TO 37 CFR 1.116

Assistant Commissioner for Patents
Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 on 1-2-03.


Ann Trent

Dear Sir:

Responsive to the Office Action mailed November 1, 2002, in connection with the above identified application, Applicants respectfully submit the following amendments and remarks.

RECEIVED
JAN 10 2003
TECHNOLOGY CENTER 2800